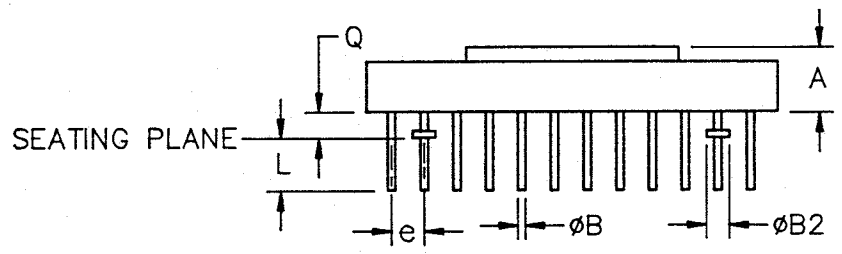
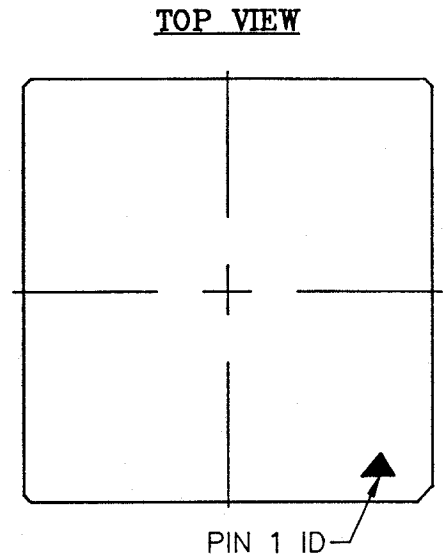
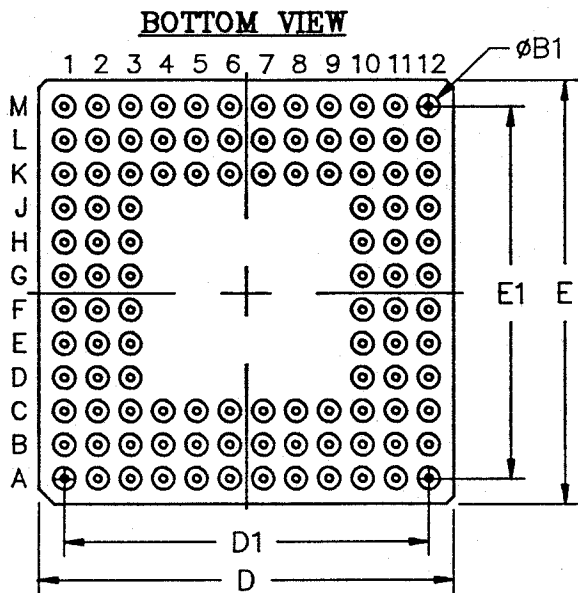



REV	DCN	DESCRIPTION	DATE	APPROVAL
01	17258	UPDATED TO STANDARDIZE DWG	1-21-90	S Thomas



- NOTES: (UNLESS OTHERWISE SPECIFIED)
1. ALL DIMENSIONS ARE IN INCHES.
 2. BSC - BASIC LEAD SPACING BETWEEN CENTERS
 3. SYMBOL "M" REPRESENTS THE PGA MATRIX SIZE.
 4. SYMBOL "N" REPRESENTS THE NUMBER OF PINS.
 5. CHAMFERED CORNERS ARE IDT'S OPTION.

SYMBOL	MIN	MAX		CONFIGURATION	EXCEPTIONS	
A	.070	.145	MIL-M-38510	SMALL OUTLINE P-BD	NONE	
ϕB	.016	.020	JEDEC	MO-066-AD	NONE	
$\phi B1$.080	TOLERANCES UNLESS OTHERWISE SPECIFIED FRAC DEC ANGLES ± - ± - ± -	 Integrated Device Technology, Inc. 3236 Scott Blvd., Santa Clara, CA 95051 (408) 727-6116 FAX: (408) 727-2328		
$\phi B2$.040	.060				
D/E	1.188	1.212				
D1/E1	1.100 BSC					
e	.100 BSC		APPROVALS	DATE	108 PIN PGA MKT DWG (CAVITY UP)	
L	.120	.140	DRAWN <i>Ad</i>	01/90		
M	12		CHECKED <i>Djs</i>	1/90		
N	108					
Q	.040	.060				
			SCALE	SIZE	DRAWING NO.	REV
			N/A	A	PSC-2078	01
DO NOT SCALE DRAWING					SHEET 2 OF 2	