## Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 9FGV1001B002NBGI. Please refer to the device datasheet for further information about the device.

#### **General Configuration**

Parameter	Value	Units
Device I <sup>2</sup> C Address	Primary 0xD0	
Crystal Load Capacitance	8	pF

## Frequency Overview

Parameter	Configuration 0	Configuration 1	Configuration 2	Configuration 3	Units
Input	25	25	25	25	MHz
REF 0	_	_	_	_	MHz
REF 1	_	_	_	_	MHz
Output 0	50	100	125	156.25	MHz
Output 1	50	100	125	156.25	MHz
Output 2	50	100	125	156.25	MHz
Output 3	50	100	125	156.25	MHz

Configuration 0 Parameters: SEL[1:0] = 00

Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	1.8	1.8	1.8	1.8	1.8	1.8	V
Output Type	LVCMOS	LVCMOS	LP-HCSL	LP-HCSL	LP-HCSL	LP-HCSL	
Frequency	_	_	50	50	50	50	MHz
LP-HCSL Impedance	_	_	100	100	100	100	Ω



# Configuration 1 Parameters: SEL[1:0] = 01

Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	1.8	1.8	1.8	1.8	1.8	1.8	V
Output Type	LVCMOS	LVCMOS	LP-HCSL	LP-HCSL	LP-HCSL	LP-HCSL	
Frequency	_	_	100	100	100	100	MHz
LP-HCSL Impedance	_	_	100	100	100	100	Ω

## Configuration 2 Parameters: SEL[1:0] = 10

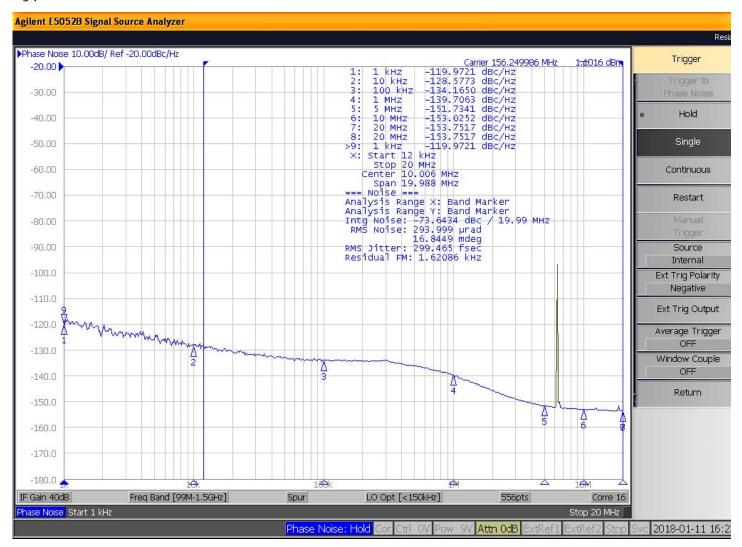
Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	1.8	1.8	1.8	1.8	1.8	1.8	V
Output Type	LVCMOS	LVCMOS	LP-HCSL	LP-HCSL	LP-HCSL	LP-HCSL	
Frequency	_	_	125	125	125	125	MHz
LP-HCSL Impedance	_	_	100	100	100	100	Ω

# Configuration 3 Parameters: SEL[1:0] = 11

Parameter	REF 0	REF 1	Output 0	Output 1	Output 2	Output 3	Units
Crystal Frequency	25	25	25	25	25	25	MHz
Default Output Status	Off	Off	On	On	On	On	
VDDO Voltage	1.8	1.8	1.8	1.8	1.8	1.8	V
Output Type	LVCMOS	LVCMOS	LP-HCSL	LP-HCSL	LP-HCSL	LP-HCSL	
Frequency	_	_	156.25	156.25	156.25	156.25	MHz
LP-HCSL Impedance	_	_	100	100	100	100	Ω



#### Typical Phase Jitter at 156.25MHz



# 9FGV1001B002NBGI Ordering Information

Orderable Part Number	Marking	Package	Carrier Type	Temperature
9FGV1001B002NBGI	1002l YWW xxx	4 × 4 mm, 0.5mm pitch 24-VFQFPN	Tray	-40° to +85°C
9FGV1001B002NBGI8	1002l YWW xxx	4 × 4 mm, 0.5mm pitch 24-VFQFPN	Tape and Reel	-40° to +85°C

#### Marking notes:

<sup>&</sup>lt;sup>1</sup> Line 1: truncated part number.

<sup>&</sup>lt;sup>2</sup> "YWW" is the last digit of the year and work week that the part was assembled.

<sup>&</sup>lt;sup>3</sup> "xxx" denotes the lot number.



## **Revision History**

Revision Date	Description of Change
May 16, 2018	Initial release.



Corporate Headquarters 6024 Silver Creek Valley Road San Jose, CA 95138 USA www.IDT.com Sales 1-800-345-7015 or 408-284-8200 Fax: 408-284-2775

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