

## General Configuration

| Parameter                            | Value                                   |
|--------------------------------------|---|
| Device Version                       | XF version (150fs typical phase jitter) |
| Package                              | 2.5 × 2.0 mm                            |
| Output Frequency                     | 312.500000MHz                           |
| Frequency Stability                  | ±25ppm                                  |
| VDD Voltage                          | 3.3V                                    |
| Output Type                          | LVPECL                                  |
| Output Enable/Disable (E/D) Position | Pin 5                                   |
| Temperature Grade                    | -40°C to +85°C                          |

## Pin Assignments

| Number        | Name  | Description                                       |
|---------------|-------|---|
| 5             | E/D   | Enable/Disable <sup>1</sup> (0 = output disable). |
| 1, 2, 3, 4, 6 | NC    | No connect.                                       |
| 10, 12, EPAD  | GND   | Connect to ground.                                |
| 8             | OUT1  | Output.   |
| 9             | OUT1b | Complementary Output.                             |
| 7, 11         | VDD   | Supply voltage.                                   |

<sup>1</sup> Pulled high internally.

## Revision History

| Revision Date    | Description of Change |
|------------------|-----------------------|
| December 6, 2018 | Initial release.      |



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