

8V97051 Evaluation Board User Guide

Introduction

The 8V97051 evaluation board is designed to help the customer evaluate the 8V97051 and 8V97053 IDT Wideband RF synthesizers. When the board is connected to a PC running IDT Timing Commander Software through USB, the device can be configured and programmed to generate frequencies with best-in-class performances.

Requirements

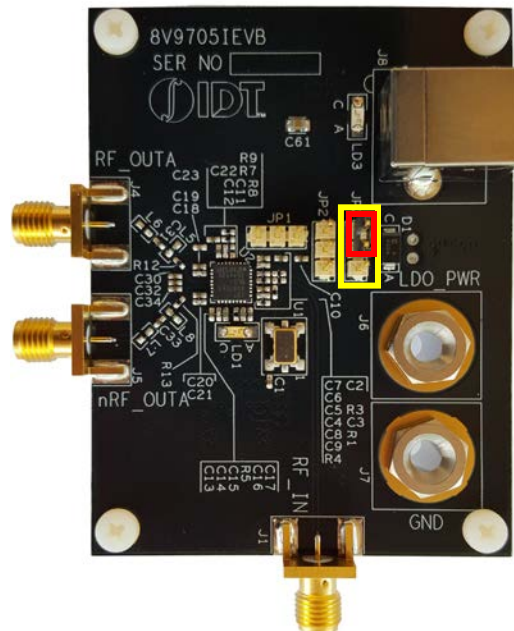
PC Requirements:

- IDT Timing Commander Software Installed.
- USB 2.0 or USB 3.0 interface.
- Windows XP SP3 or later.
- Processor: Minimum 1GHz.
- Memory: Minimum 512MB, recommended 1GB.
- Available Disk Space: Min 600MB (1.5GB 64bit), recommended 1GB (2GB 64bit)
- Network access during installation if the .NET framework is not currently installed on the system.

Quick Start

- (1) Connect a cable from a PC to the Evaluation Board USB port.
- (2) Verify that the board is configured to power from the USB (see JP3 jumper position in Fig.1)
- (3) JP2 and JP3 can be left floating.
- (4) Connect 50ohm cables from the RF_OUTA output to the measurement equipment.
- (5) Configure the device using Timing Commander.

Figure 1 8V97051 EVB



Default Power-Up Condition

The board ships with a 25MHz TXCO and is configured to power from the USB. No external reference is required and no external power source, aside from the USB connection, is required. Outputs are disabled by default. The device must be configured using Timing Commander in order to activate the output.

Note: this device also has an RF_OUTb differential output that is not routed out on the evaluation board.

For more questions or support, feel free to contact us at clocks@idt.com



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