



## 1. Introduction

This document summarizes the differences between the Tundra Tsi382 and Tsi382A. Topics discussed include the following:

- [Functional Differences](#)
- [Register Differences](#)
- [Signal/Pinout Differences](#)
- [Errata Differences](#)
- [Other Differences](#)

### 1.1 Revision History

April 21, 2014

This version of the document was rebranded as IDT. No technical changes were made.

May 19, 2009

This is the first version of the *Tsi382/Tsi382A Device Differences*.

## 2. Functional Differences

Table 1 Functional Differences

Function/Feature	Tsi382	Tsi382A
Legacy mode (Subtractive decode support) <sup>1</sup>		✓
SSID/SSVID support <sup>1</sup>		✓
Opaque addressing mode <sup>2</sup>	✓	

<sup>1</sup> For information on this feature, see the *Tsi382A User Manual*.

<sup>2</sup> For information on this feature, see the *Tsi382 User Manual*.

## 3. Register Differences

Table 2 Register Differences

Offset	Tsi382 Register Space	Tsi382A Register Space	Bit(s)
0x008	PCI Class Register		15:0
0x034	PCI Capability Pointer Register		7:0
0x048	PCI Miscellaneous Clock Straps Register		7

Table 2 Register Differences

Offset	Tsi382 Register Space	Tsi382A Register Space	Bit(s)
0x058	SERRDIS_OPQEN_DTC Register		5
0x05C	Opaque Memory Lower Register	N/A	All
0x060	Opaque Memory Upper Base Register	SSID Capability Register	All
0x064	Opaque Memory Limit Base Register	SSID ID Register	All

### 3.1 Signal/Pinout Differences

There are no signal or pinout differences between the Tsi382 and Tsi382A.

### 3.2 Errata Differences

The following Tsi382 device errata are fixed in the Tsi382A:

- [PCIE1] – L1 exit is not initiated when a TLP completion must be returned
- [PCIE2] – Device erroneously responds to PCI Type 1 configuration accesses while in D3hot state
- [PCIE3] – Device fails to return PCIe link to L1 state after a PCI configuration access while in D3hot state

### 3.3 Other Differences

The JTAG version number is different for the devices: it is 0x03 for the Tsi382A, and 0x02 for the Tsi382.



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