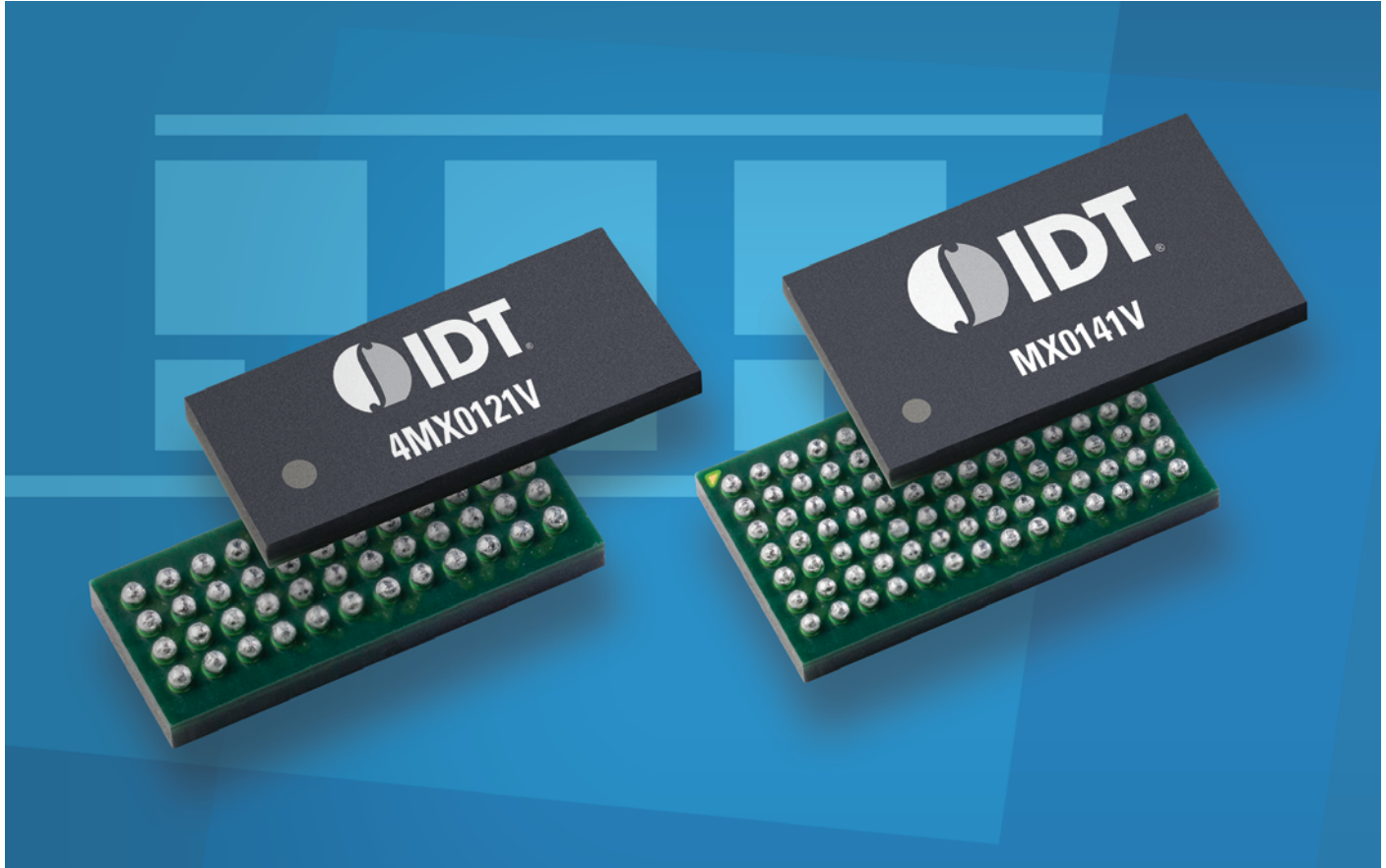


# Memory Multiplexer Family Overview



### FAMILY FEATURES AND BENEFITS

- Pinouts for easy integration into existing memory interface applications
- Small package sizes for high density applications
- Low power consumption for greater density within existing power envelopes
- High-speed switch architecture with high bandwidth, low insertion loss, return loss, and very low propagation delay

IDT memory multiplexers address the industry's growing need for memory expansion in SSD and NVDIMM applications.

IDT's 1:4 bidirectional MUX can be used to increase SSD drive capacity while allowing the SSD NV Controller to maintain a lower pin count, resulting in overall cost and power savings.

IDT's 1:2 multiplexers route DRAM data to Flash during a power outage condition in NVDIMM applications. The MUX pinout allows for placement very close to the edge connector, alleviating board constraint problems while providing minimal disturbance to high-speed signals.

Part Number	1:N	Interface Technology	Port Count	3dB BW	Package
4MX0121V	1:2	1.2V	12	2GHz	3.0 x 8.0 mm VFBGA
MX0141V	1:4	1.8V and 1.2V	12 High Speed 4 Low Speed	450MHz 400MHz	4.0 x 7.5 mm FCCSP