Note: This is the ballmap when looking through the top of the package. This ballmap represents the PCB footprint for the Tsi578.

**Legend**
- VSS and VSS_IO
- VDD and VDD_IO
- SP_VDD and SPx_AVDD
- Serial RapidIO Interface 0
- Serial RapidIO Interface 8
- Serial RapidIO Interface 10
- Serial RapidIO Interface 12
- No Ball and No Connect
- Miscellaneous
- S_CLK_x

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**Revision Notes**
August 2009, B8M024A_PN003_04 – No technical changes. Formatting was changed to reflect IDT.