With its winning combination of low phase jitter and power consumption, the VersaClock 6 device is ideal for such markets as high-end consumer, networking, computing, industrial and communications.

The VersaClock 6 programmable clock generator family delivers flexible, low-power timing for demanding high-performance applications. With RMS phase jitter of less than 500 femtoseconds (fsec), the VersaClock 6 products deliver the best performance to date for IDT’s award-winning VersaClock family, offering an exceptional combination of jitter performance, flexibility, and low operating power.

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Functionality</th>
<th>Output Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>5P49V6901</td>
<td>PLL with 4 FOD</td>
<td>4 configurable output pairs + reference output</td>
</tr>
<tr>
<td>5P49V6913</td>
<td>PLL with 2 FOD</td>
<td>2 configurable output pairs + reference output</td>
</tr>
<tr>
<td>5P49V6914</td>
<td>PLL with 3 FOD</td>
<td>3 configurable output pairs + reference output</td>
</tr>
</tbody>
</table>

To request samples, download documentation, or learn more, visit: idt.com/go/versaclock6