



Layout notes:

- 1 Route power from bead through bulk capacitor pad then through 0.1uF capacitor pad then to clock chip Vdd pad.
- 2 Do not share ground vias. One ground pin one ground via.

NOTE:FERRITE BEAD FB1 = Z@100MHz

Manufacture	Part Number	PkgSz	DC res.	Current (Ma)
Fair-Rite	2504021217Y0	0402	0.5	200
TDK	MMZ11005S241A	0402	0.18	200
muRata	B1M15AG221SN1	0402	0.35	300
muRata	BLM15BB121SN1	0402	0.35	300
TECSTAR	TB4532153121	0402	0.3	300

Revision history

0.1 03/26/2015 First publication

0.2 04/25/16 pin out correction

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