



FOR 9DBV0441:  
Series resistors are internal.  
They are not needed in schematic.

FOR 9DBV0431:  
TO CONFIGURE FOR Zdiff = 85 Ohms  
Rs = 27 Ohms  
TO CONFIGURE FOR Zdiff = 100 Ohms  
Rs = 33 Ohms

NOTE: FERRITE BEADS FB1 =

Manufacturer	Part Number	Z@100MHz	PkgSz	DC res.	Current (Ma)
muRata	BLM21A601R	600	0805	0.30	600
TDK	MMZ2012S601A	600	0805	0.30	600
STEWART	HZ0805E601R	600	0805	0.30	600
AssocCmpTch	CBG0805-600-50	600	0805	0.30	600

Manufacturer	Part Number	Z@100MHz	PkgSz	DC res.	Current (Ma)
muRata	BLM18AG601SN1	600	1608	0.50	200
muRata	BLM18BD601SN1_PB	600	1608	0.65	200
Ceratech	HB-1T1608-601-	600	1608	0.50	200
TDK	MMZ1608R301A	300	1608	0.20	500

Revision history  
0.1 8/31/2012 first publication

- Layout notes.
1. Route power from bead through bulk capacitor pad then through 0.1uF capacitor pad then to clock chip Vdd pad.
  2. Do not share ground vias. One ground pin one ground via.

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