



### Bootup Configuration

Device 8-bit I2C address	Default Configuration
0xf8	EEPROM (8-bit EEPROM address 0xa0)

### Inputs

Crystal Frequency	Input CLK0	Input CLK1
38.88MHz	—	—

### Outputs

Output	Frequency (MHz)	Type	Enable	Polarity	Voltage	Source	Divider	Err
Q0	—	High-Z	Disabled	Normal	3.3	PLL	—	—
Q1	—	High-Z	Disabled	Normal	3.3	PLL	—	—
Q2	—	High-Z	Disabled	Normal	3.3	PLL	—	—
Q3	—	High-Z	Disabled	Normal	3.3	PLL	—	—

### PLL

Frequency	Mode	Acquire BW	Lock BW	CLK0 PFD	CLK1 PFD
—	Synthesizer	25Hz	25Hz	—	—

### Power

Typical Power	Max Power
—	—

### GPIOs

GPIO0	GPIO1	GPIO2	GPIO3
In – OE0	In – OE1	In – CSEL	In – GPI

### Registers

0A 50 00 60 70 0D 7C 01 03 00 31 00 00 01 00 00
01 07 00 00 07 00 00 77 6D 06 00 00 00 00 00 FF
FF FF FF 00 3F 00 2D 00 00 00 00 00 01 00 00 10
00 00 00 07 00 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
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00 83 00 10 02 08 8C



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