

Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 9FGV1005Q505LTGI. Please refer to the device datasheet for further information about the device.

General Configuration

Parameter	Value	Units
Device I ² C Address	Primary 0xD0	
Crystal Load Capacitance	8	pF

Frequency Overview

Parameter	Configuration 0	Configuration 1	Configuration 2	Configuration 3	Units
Internal Crystal	50	50	50	50	MHz
REF 0	Off	Off	Off	Off	MHz
Output 0	50	100	125	156.25	MHz
Output 1	50	100	125	156.25	MHz

Configuration 0 Parameters: SEL[1:0] = 00

Parameter	REF 0	Output 0	Output 1	Units
Crystal Frequency	50	50	50	MHz
Default Output Status	Off	On	On	
VDDO Voltage	3.3	3.3	3.3	V
Output Type	LVC MOS	LP-HCSL	LP-HCSL	
Frequency	—	50	50	MHz
LP-HCSL Impedance	—	100	100	Ω

Configuration 1 Parameters: SEL[1:0] = 01

Parameter	REF 0	Output 0	Output 1	Units
Crystal Frequency	50	50	50	MHz
Default Output Status	Off	On	On	
VDDO Voltage	3.3	3.3	3.3	V
Output Type	LVC MOS	LP-HCSL	LP-HCSL	
Frequency	—	100	100	MHz
LP-HCSL Impedance	—	100	100	Ω

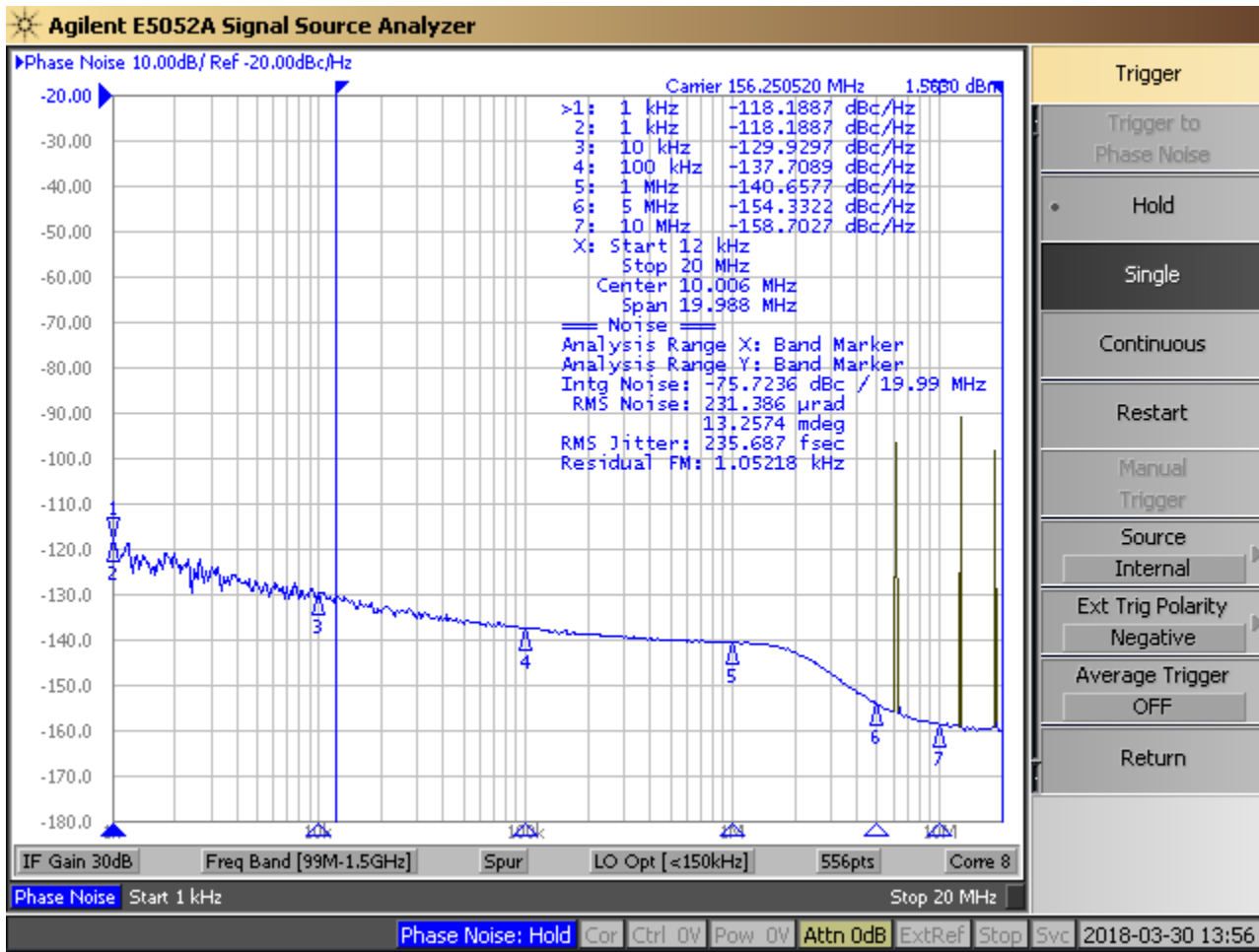
Configuration 2 Parameters: SEL[1:0] = 10

Parameter	REF 0	Output 0	Output 1	Units
Crystal Frequency	50	50	50	MHz
Default Output Status	Off	On	On	
VDDO Voltage	3.3	3.3	3.3	V
Output Type	LVC MOS	LP-HCSL	LP-HCSL	
Frequency	—	125	125	MHz
LP-HCSL Impedance	—	100	100	Ω

Configuration 3 Parameters: SEL[1:0] = 11

Parameter	REF 0	Output 0	Output 1	Units
Crystal Frequency	50	50	50	MHz
Default Output Status	Off	On	On	
VDDO Voltage	3.3	3.3	3.3	V
Output Type	LVC MOS	LP-HCSL	LP-HCSL	
Frequency	—	156.25	156.25	MHz
LP-HCSL Impedance	—	100	100	Ω

Typical Phase Jitter at 156.25MHz



9FGV1005Q505LTGI Ordering Information

Orderable Part Number	Marking	Package	Carrier Type	Temperature
9FGV1005Q505LTGI	55051 YWW xxx	3 × 3 mm, 0.5mm pitch 16-LGA	Tray	-40° to +85°C
9FGV1005Q505LTGI8	55051 YWW xxx	3 × 3 mm, 0.5mm pitch 16-LGA	Tape and Reel	-40° to +85°C

Marking notes:

- ¹ Line 1: truncated part number.
- ² "YWW" is the last digit of the year and work week that the part was assembled.
- ³ "xxx" denotes the lot number.

Revision History

Revision Date	Description of Change
May 14, 2018	Initial release.

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