

**FEATURES:**

- N channel FET switches with no parasitic diode to Vcc
  - Isolation under power-off conditions
  - No DC path to Vcc or GND
  - 5V tolerant in OFF and ON state
- 5V tolerant I/Os
- Low RON - 4Ω typical
- Flat RON characteristics over operating range
- Rail-to-rail switching 0 - 5V
- Bidirectional dataflow with near-zero delay: no added ground bounce
- Excellent RON matching between channels
- VCC operation: 2.3V to 3.6V
- High bandwidth - up to 500MHz
- LVTTTL-compatible control Inputs
- Undershoot Clamp Diodes on all switch and control Inputs
- Low I/O capacitance, 4pF typical
- Available in QSOP package

**APPLICATIONS:**

- Hot-swapping
- 10/100 Base-T, Ethernet LAN switch
- Low distortion analog switch
- Replaces mechanical relay
- ATM 25/155 switching

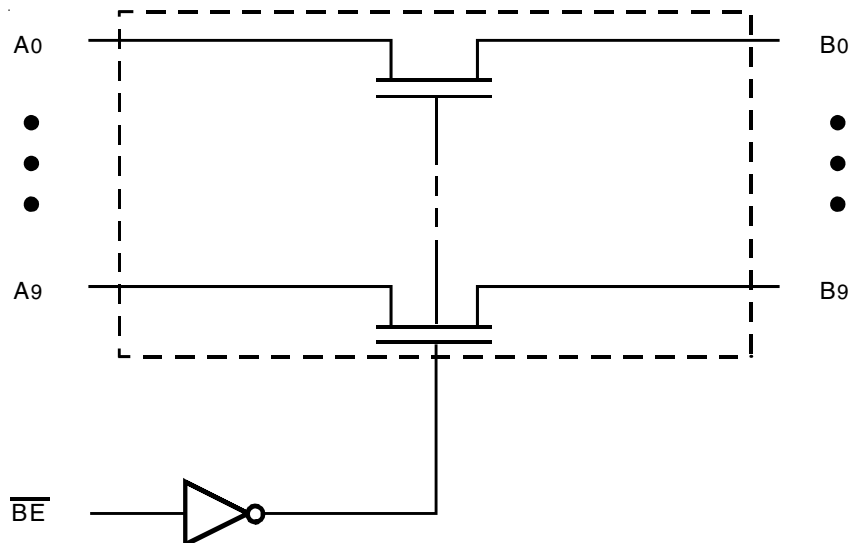
**DESCRIPTION:**

The QS3VH861 HotSwitch with 10-bit flow-through pin out is a high bandwidth bus switch. The QS3VH861 has very low ON resistance, resulting in under 250ps propagation delay through the switch. The switches are controlled by active low enable (BE) control. In the ON state, the switches can pass signals up to 5V. In the OFF state, the switches offer very high impedance at the terminals.

The combination of near-zero propagation delay, high OFF impedance, and over-voltage tolerance makes the QS3VH861 ideal for high performance communications applications.

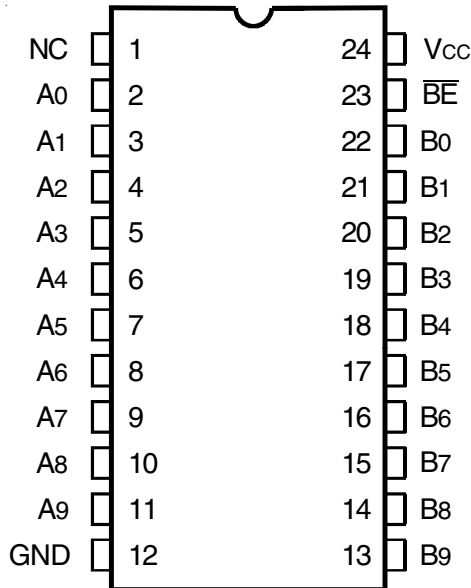
The QS3VH861 is characterized for operation from -40°C to +85°C.

**FUNCTIONAL BLOCK DIAGRAM**



The IDT logo is a registered trademark of Integrated Device Technology, Inc.

### PIN CONFIGURATION



QSOP  
TOP VIEW

### ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
VTERM(2)	Supply Voltage to Ground	-0.5 to +4.6	V
VTERM(3)	DC Switch Voltage Vs	-0.5 to +5.5	V
VTERM(3)	DC Input Voltage VIN	-0.5 to +5.5	V
VAC	AC Input Voltage (pulse width ≤20ns)	-3	V
IOUT	DC Output Current (max. sink current/pin)	120	mA
TSTG	Storage Temperature	-65 to +150	°C

**NOTES:**

1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Vcc terminals.
3. All terminals except Vcc .

### CAPACITANCE (TA = +25°C, F = 1MHz, VIN = 0V, VOUT = 0V)

Symbol	Parameter(1)	Typ.	Max.	Unit
CIN	Control Inputs	3	5	pF
C <sub>I/O</sub>	Quickswitch Channels (Switch OFF)	4	6	pF
C <sub>I/O</sub>	Quickswitch Channels (Switch ON)	8	12	pF

**NOTE:**

1. This parameter is guaranteed but not production tested.

### PIN DESCRIPTION

Pin Names	Description
BE	Active LOW Bus Enable
A0 - A9	Bus A
B0 - B9	Bus B

### FUNCTION TABLE(1)

BE	A0 - A9	Function
H	Z	Disconnect
L	B0 - B9	Connect

**NOTE:**

1. H = HIGH Voltage Level  
L = LOW Voltage Level  
Z = High-Impedence

## DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

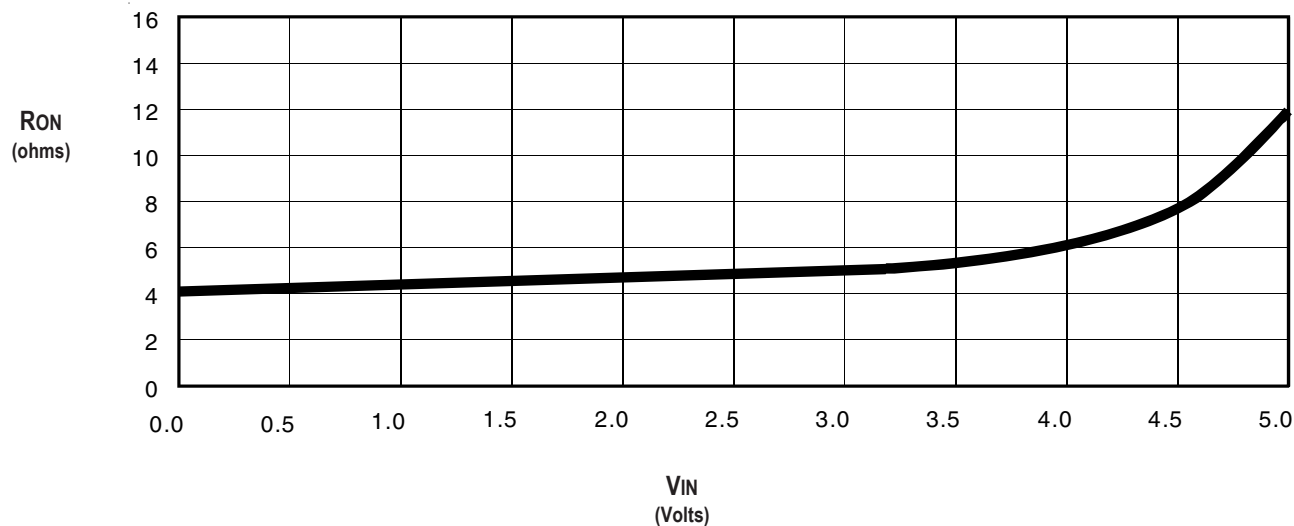
Industrial:  $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{CC} = 3.3\text{V} \pm 0.3\text{V}$

Symbol	Parameter	Test Conditions			Min.	Typ. <sup>(1)</sup>	Max.	Unit
$V_{IH}$	Input HIGH Voltage	Guaranteed Logic HIGH for Control Inputs	$V_{CC} = 2.3\text{V}$ to $2.7\text{V}$		1.7	—	—	V
			$V_{CC} = 2.7\text{V}$ to $3.6\text{V}$		2	—	—	
$V_{IL}$	Input LOW Voltage	Guaranteed Logic LOW for Control Inputs	$V_{CC} = 2.3\text{V}$ to $2.7\text{V}$		—	—	0.7	V
			$V_{CC} = 2.7\text{V}$ to $3.6\text{V}$		—	—	0.8	
$I_{IN}$	Input Leakage Current (Control Inputs)	$0\text{V} \leq V_{IN} \leq V_{CC}$			—	—	$\pm 1$	$\mu\text{A}$
$I_{OZ}$	Off-State Current (Hi-Z)	$0\text{V} \leq V_{OUT} \leq 5\text{V}$ , Switches OFF			—	—	$\pm 1$	$\mu\text{A}$
$I_{OFF}$	Data Input/Output Power Off Leakage	$V_{IN}$ or $V_{OUT}$ $0\text{V}$ to $5\text{V}$ , $V_{CC} = 0\text{V}$			—	—	$\pm 1$	$\mu\text{A}$
$R_{ON}$	Switch ON Resistance	$V_{CC} = 2.3\text{V}$ Typical at $V_{CC} = 2.5\text{V}$	$V_{IN} = 0\text{V}$	$I_{ON} = 30\text{mA}$	—	6	8	$\Omega$
			$V_{IN} = 1.7\text{V}$	$I_{ON} = 15\text{mA}$	—	7	9	
		$V_{CC} = 3\text{V}$	$V_{IN} = 0\text{V}$	$I_{ON} = 30\text{mA}$	—	4	6	
			$V_{IN} = 2.4\text{V}$	$I_{ON} = 15\text{mA}$	—	5	8	

### NOTE:

1. Typical values are at  $V_{CC} = 3.3\text{V}$  and  $T_A = 25^{\circ}\text{C}$ .

## TYPICAL ON RESISTANCE vs $V_{IN}$ AT $V_{CC} = 3.3\text{V}$



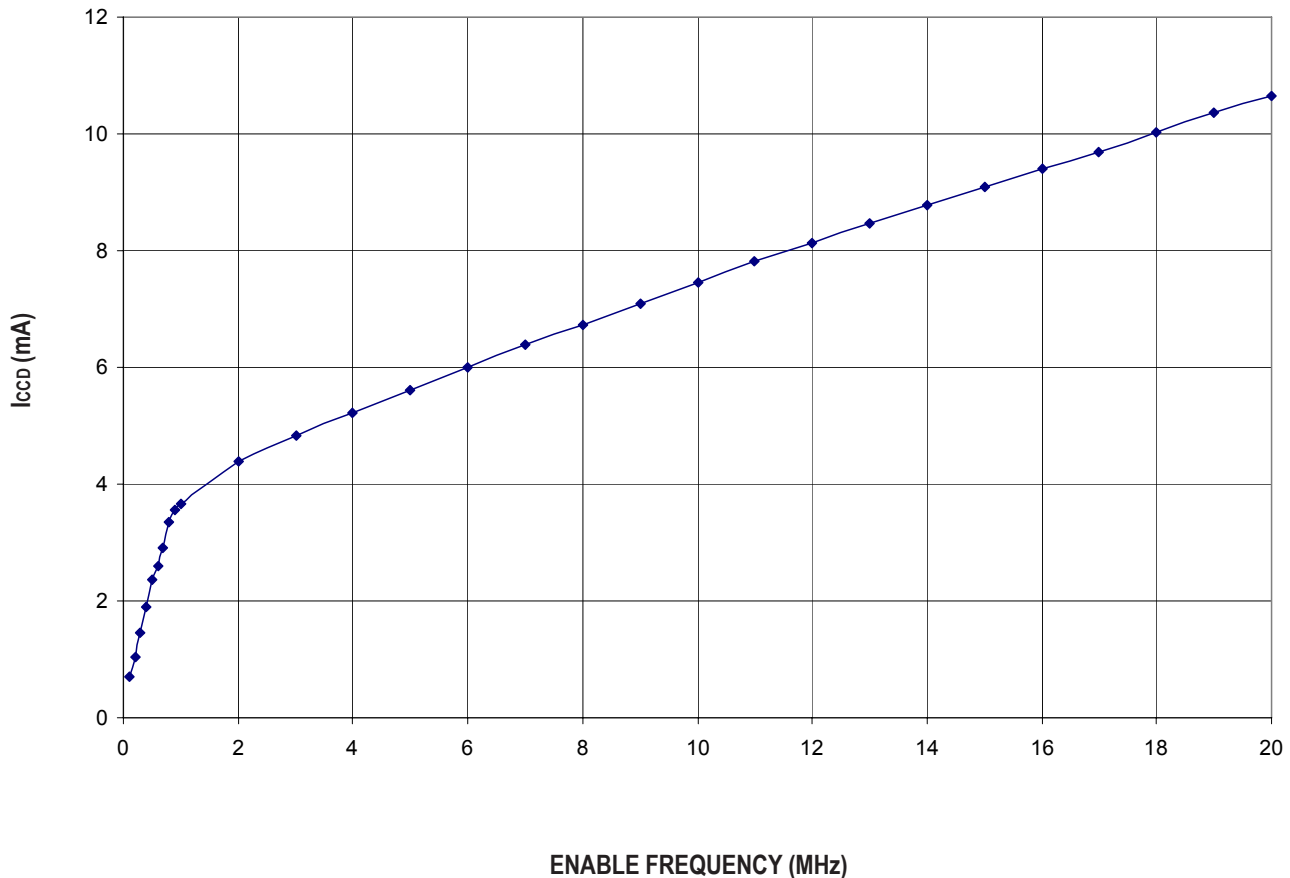
## POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions <sup>(1)</sup>	Min.	Typ.	Max.	Unit
I <sub>CCQ</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = Max., V <sub>IN</sub> = GND or V <sub>CC</sub> , f = 0	—	2	4	mA
ΔI <sub>CC</sub>	Power Supply Current <sup>(2,3)</sup> per Input HIGH	V <sub>CC</sub> = Max., V <sub>IN</sub> = 3V, f = 0 per Control Input	—	—	30	μA
I <sub>CCD</sub>	Dynamic Power Supply Current <sup>(4)</sup>	V <sub>CC</sub> = 3.3V, A and B Pins Open, Control Inputs Toggling @ 50% Duty Cycle	See Typical I <sub>CCD</sub> vs Enable Frequency graph below			

### NOTES:

- For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- Per input driven at the specified level. A and B pins do not contribute to ΔI<sub>CC</sub>.
- This parameter is guaranteed but not tested.
- This parameter represents the current required to switch internal capacitance at the specified frequency. The A and B inputs do not contribute to the Dynamic Power Supply Current. This parameter is guaranteed but not production tested.

## TYPICAL I<sub>CCD</sub> vs ENABLE FREQUENCY CURVE AT V<sub>CC</sub> = 3.3V



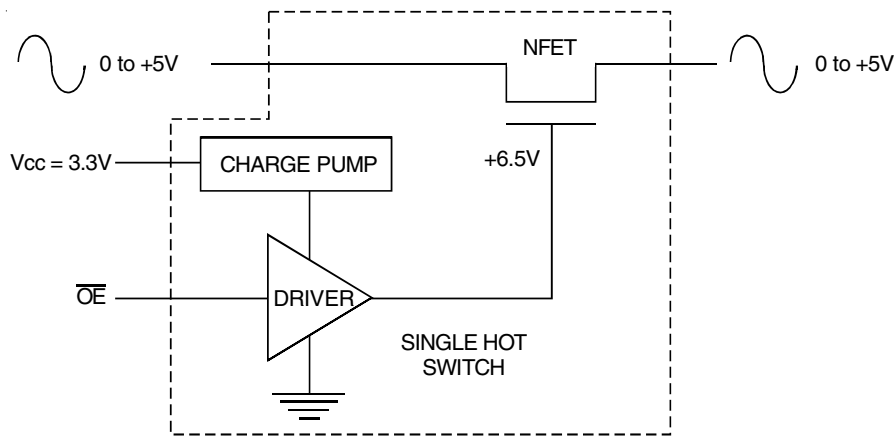
**SWITCHING CHARACTERISTICS OVER OPERATING RANGE**T<sub>A</sub> = -40°C to +85°C

Symbol	Parameter	V <sub>CC</sub> = 2.5 ± 0.2V <sup>(1)</sup>		V <sub>CC</sub> = 3.3 ± 0.3V <sup>(1)</sup>		Unit
		Min. <sup>(4)</sup>	Max.	Min. <sup>(4)</sup>	Max.	
t <sub>PLH</sub> t <sub>PHL</sub>	Data Propagation Delay <sup>(2,3)</sup> A to B or B to A	—	0.2	—	0.2	ns
t <sub>PZH</sub> t <sub>PZL</sub>	Switch Turn-On Delay $\overline{BE}$ to xA or xB	1.5	8	1.5	7	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Switch Turn-Off Delay $\overline{BE}$ to xA or xB	1.5	7	1.5	6.5	ns
f <sub>BE</sub>	Operating Frequency - Enable <sup>(2,5)</sup>	—	10	—	20	MHz

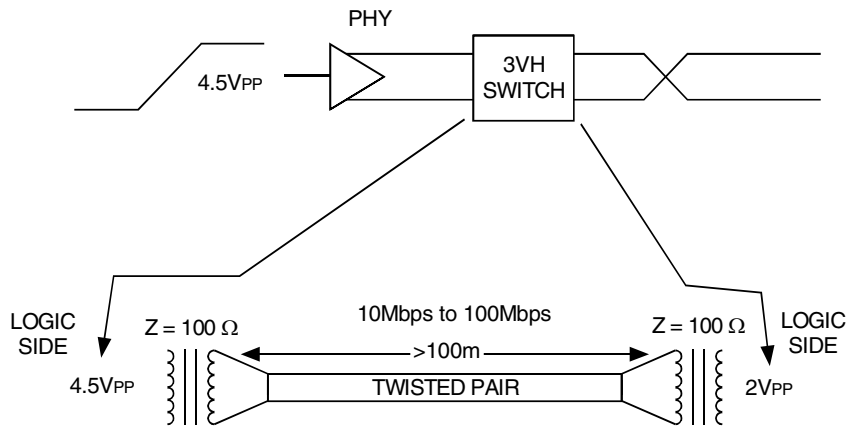
**NOTES:**

1. See Test Conditions under TEST CIRCUITS AND WAVEFORMS.
2. This parameter is guaranteed but not production tested.
3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.2ns at C<sub>L</sub> = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
4. Minimums are guaranteed but not production tested.
5. Maximum toggle frequency for  $\overline{BE}$  control input (pass voltage > V<sub>CC</sub>, V<sub>IN</sub> = 5V, R<sub>LOAD</sub> ≥ 1MΩ, no C<sub>LOAD</sub>).

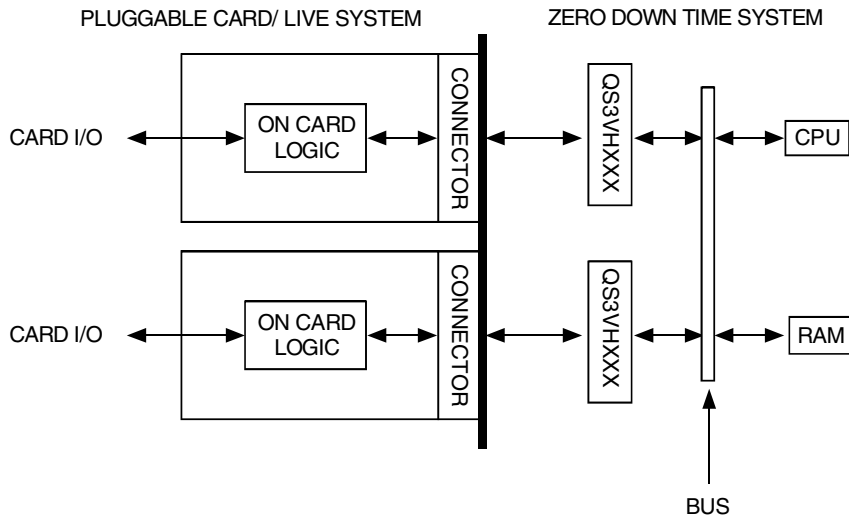
### SOME APPLICATIONS FOR HOTSWITCH PRODUCTS



*Rail-to-Rail Switching*



*Fast Ethernet Data Switching (LAN Switch)*

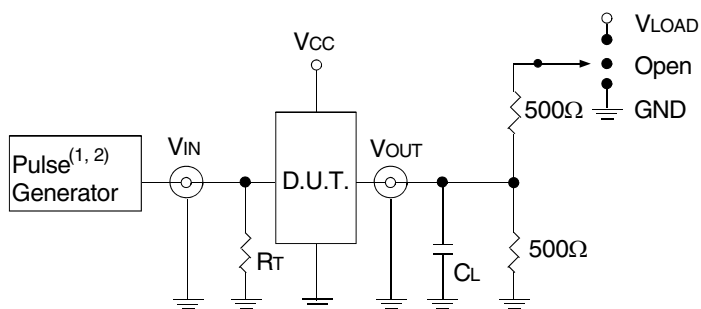


*Hot-Swapping*

## TEST CIRCUITS AND WAVEFORMS

### TEST CONDITIONS

Symbol	Vcc <sup>(1)</sup> = 3.3V ± 0.3V	Vcc <sup>(2)</sup> = 2.5V ± 0.2V	Unit
V <sub>LOAD</sub>	6	2 x V <sub>cc</sub>	V
V <sub>IH</sub>	3	V <sub>cc</sub>	V
V <sub>T</sub>	1.5	V <sub>cc</sub> /2	V
V <sub>LZ</sub>	300	150	mV
V <sub>HZ</sub>	300	150	mV
C <sub>L</sub>	50	30	pF



Test Circuits for All Outputs

#### DEFINITIONS:

C<sub>L</sub> = Load capacitance: includes jig and probe capacitance.

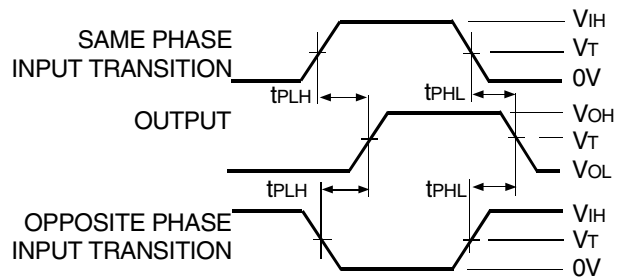
R<sub>T</sub> = Termination resistance: should be equal to Z<sub>OUT</sub> of the Pulse Generator.

#### NOTES:

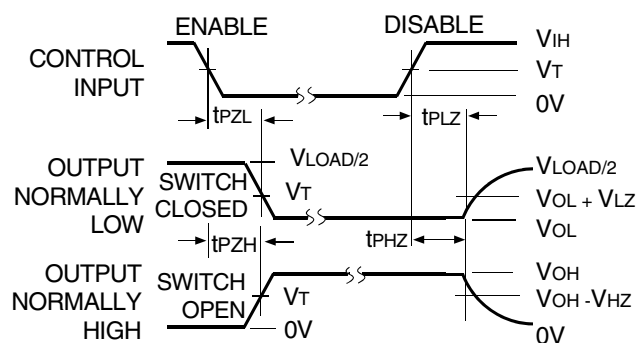
1. Pulse Generator for All Pulses: Rate ≤ 10MHz; t<sub>r</sub> ≤ 2.5ns; t<sub>r</sub> ≤ 2.5ns.
2. Pulse Generator for All Pulses: Rate ≤ 10MHz; t<sub>r</sub> ≤ 2ns; t<sub>r</sub> ≤ 2ns.

### SWITCH POSITION

Test	Switch
t <sub>PLZ</sub> /t <sub>PZL</sub>	V <sub>LOAD</sub>
t <sub>PHZ</sub> /t <sub>PZH</sub>	GND
t <sub>PD</sub>	Open



Propagation Delay

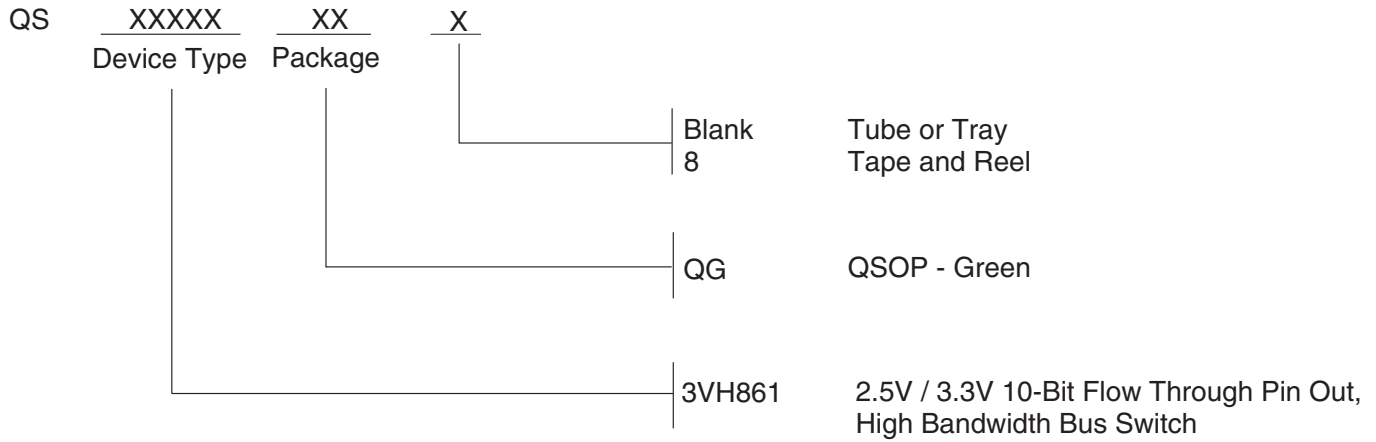


#### NOTE:

1. Diagram shown for input Control Enable-LOW and input Control Disable-HIGH.

#### Enable and Disable Times

## ORDERING INFORMATION



## Datasheet Document History

09/01/08	Pg. 4, 8	Revise ICCQ Typ. and Max. Remove non green package version and updated the ordering information by removing the "IDT" notation.
02/24/14	Pg. 8	Updated the Ordering Information by Adding Tape and Reel information.



## Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
  2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
  3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
  4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
  5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
    - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
    - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
  6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
  7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
  8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
  9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
  10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
  11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
  12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.
- (Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.
- (Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)

## Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,  
Koto-ku, Tokyo 135-0061, Japan  
[www.renesas.com](http://www.renesas.com)

## Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:  
[www.renesas.com/contact/](http://www.renesas.com/contact/)

## Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.