

Supplemental Information

This Device Errata reflects revision 1.0 silicon and supplement information found in the documentation for this device.

Z step is revision 1.0. This is identified by material date code (top mark) of 0227 or later.

Document Revision History

Dec 13, 2006 : First version of the document.

Feb 6, 2007 : Second version of the document.

Description and Workarounds

Item #1: XTAL Interface

Issue: Under certain process conditions a reduced XTAL_OUT level is observed which could cause the output to switch at the wrong frequency or to be stuck low or high.

Use of XTAL interface is not recommended for this device. Functionality with REF_SEL = 1 will not be supported.

Workaround: Device must be used with REF_SEL = 0, selecting CCLK0 or CCLK1 as the input.

Fix: There are no plans to fix this device. Product ICS87972DYI-147 is recommended as pin compatible replacement if XTAL interface is required.