



Integrated Device Technology, Inc.
2975 Stender Way, Santa Clara, CA - 95054

PRODUCT/PROCESS CHANGE NOTICE (PCN)

PCN #: **I0307-05** DATE: 8/15/2003
 Product Affected: IDT 79RC32K438

 Date Effective: 11/15/03

MEANS OF DISTINGUISHING CHANGED DEVICES:
 Product Mark
 Back Mark Not Applicable
 Date Code
 Other

Contact: Bimla Paul
 Title: QA Manager Attachment: Yes No
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 Fax #: 408-492-8362 Samples: Not Applicable
 E-mail: Bimla.Paul@idt.com

DESCRIPTION AND PURPOSE OF CHANGE:

- Die Technology
 - Wafer Fabrication Process
 - Assembly Process
 - Equipment
 - Material
 - Testing
 - Manufacturing Site
 - Data Sheet
 - Other
- To revise selected data sheet parameters. No changes have been made to the device nomenclature.

RELIABILITY/QUALIFICATION SUMMARY:

Not Applicable - Data Sheet Change

CUSTOMER ACKNOWLEDGMENT OF RECEIPT:

IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice it will be assumed that this change is acceptable.
 IDT reserves the right to ship either version manufactured after the process change effective date until the inventory on the earlier version has been depleted.

Customer: _____ *Approval for shipments prior to effective date.*
 Name/Date: _____ E-Mail Address: _____
 Title: _____ Phone# /Fax# : _____

CUSTOMER COMMENTS: _____

IDT ACKNOWLEDGMENT OF RECEIPT:

RECD. BY: _____ DATE: _____



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ATTACHMENT 1 - PCN #: I0307-05

PCN Type: Data Sheet Change

Data Sheet Change Yes

Detail of Change

FROM

Page 21: DDRDATA(31:0) - Tdo_7k (Max) = 3.2 ns (233 MHz speed grade only)
Page 21: DDRDATA(31:0) - Tdo_7k (Min) = 1.1 ns and Tdo_7k (Max) = 2.6 ns (266 MHz speed grade only)
Page 21: DDRDM(7:0) - Tdo_7l (Max) = 3.2 ns (233 MHz speed grade only)
Page 21: DDRDM(7:0) - Tdo_7l (Min) = 1.1 ns and Tdo_7l (Max) = 2.6 ns (266 MHz speed grade only)
Page 23: MADDR(21:0) - Tdo_8a (Min) = 1.2 ns (all speed grades)
Page 23: MADDR(25:22) - Tdo_8b (Min) = 1.2 ns (all speed grades)
Page 24: MDATA(15:0) - Tso_8c (Min) = 5.5 ns. (266 mHz speed grade only)
Page 24: BDIRN - Tdo_8e (Min) = 1.4 ns and Tdo_8e (Max) = 3.4 ns. (233 MHz, 266 mHz speed grades)
Page 24: BOEN - Tdo_8f (Min) = 1.6 ns and Tdo_8f (Max) = 3.8 ns. (233 MHz, 266 mHz speed grades)
Page 26: DMAFINN(1:0): Tdo_8p (Max) = 5.5 ns. (all speed grades)
Page 40: ICCSI/O - Typ = 80 mA, Max = 100mA (200MHz); Typ = 130 mA, Max = 150 mA (233MHz);
Typ = 180 mA, Max = 200 mA (266 MHZ).

TO

Page 21: DDRDATA(31:0) - Tdo_7k (Max) = 2.9 ns (233 MHz speed grade only)
Page 21: DDRDATA(31:0) - Tdo_7K (Min) = 0.9 ns and Tdo_7k (Max) = 2.7 ns (266 MHz speed grade only)
Page 21: DDRDM(7:0) - Tdo_7l (Max) = 2.9 ns (233 MHz speed grade only)
Page 21: DDRDM(7:0) - Tdo_7l (Min) = 0.9 ns and Tdo_7l (Max) = 2.7 ns (266 MHz speed grade only)
Page 23: MADDR(21:0) - Tdo_8a (Min) = 0.0 ns (all speed grades)
Page 23: MADDR(25:22) - Tdo_8b (Min) = 0.0 ns (all speed grades)
Page 24: MDATA(15:0) - Tso_8c (Min) = 7.0 ns. (266 mHz speed grade only)
Page 24: BDIRN - Tdo_8e (Min) = 1.0 ns and Tdo_8e (Max) = 4.0 ns. (233 MHz, 266 mHz speed grades)
Page 24: BOEN - Tdo_8f (Min) = 1.0 ns and Tdo_8f (Max) = 4.0 ns. (233 MHz, 266 mHz speed grades)
Page 26: DMAFINN(1:0): Tdo_8p (Max) = 6.0 ns. (all speed grades)
Page 40: ICCSI/O-Typ = 100 mA, Max = 120mA (200MHz); Typ = 150 mA, Max = 170 mA (233MHz);
Typ = 200 mA, Max = 220 mA (266 MHZ)

Pages 1, 2, 3, 9, 12, 15, 17, 32, 43, 47, and 51 - Delete all references to IP Bus Monitor.