The 4MX0121V is a 12-bit bus switch/multiplexer designed for 2.5 V or 3.3 V supply voltage operation in DDR3 and DDR4 memory bus systems.

The device has a 1:2 switch or 2:1 multiplex topology. Each 12-bit wide A-port can be switched to one of two ports B and C, for all bits simultaneously. Each port is bidirectional for high-speed and high-bandwidth switch multiplexer applications. Port selection uses two simple input selection pins and all ports can be disconnected via an enable pin. The device is divided into two 6-bit bus switch/multiplexers for additional flexibility.

The 4MX0121V uses a high-speed switch architecture providing high bandwidth, low insertion loss, low return loss, and very low propagation delay, allowing use in many applications requiring switching or multiplexing of high-speed signals. It is available in a 3 x 8 mm 48-ball VFBGA package with 0.65 mm ball pitch for optimal size versus board layout density.

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