General Description

The HXR6104 Transimpedance Limiting Amplifier array is a member of IDT’s family of Optical Receiver Transmitter Array (ORTA) products targeted at the parallel optical links market. Together with a PIN detector array or discrete detectors, high capacity, high availability optical links can be designed for telecom and datacom applications.

The 3.3V SiGe device integrates the trans-impedance pre-amplifier, the limiting post-amplifier and a versatile CML output stage for four optical input channels.

Applications

• IEEE 802.3ba Ethernet Transceivers
• InfiniBand QDR & FDR Active Cables
• Proprietary multi-channel optical modules

Features

• 20 μA_pp sensitivity for 10⁻¹² BER at 10.3 Gbps
• 60 mW/ch power consumption at low setting
• Automatic gain and threshold control
• 3 mA_pp minimum peak to peak overload
• Adjustable CML output stage amplitude
• Adjustable output pre-emphasis amplitude and duration
• Adjustable signal detect threshold & SQUELCH
• Independent RSSI per channel output
• Independent per channel temperature monitor
• Data polarity inversion per channel
• Optimized for isolated and common cathode photo-detector arrays from multiple vendors
• Control lines accessible on both sides of the die
• QSFP MSA compliance

Ordering Information

<table>
<thead>
<tr>
<th>Part</th>
<th>Temp Range</th>
<th>Pin-Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>HXR6104-DNT</td>
<td>0°C to +85°C</td>
<td>Bare Die 2.05mm x 1.67mm</td>
</tr>
<tr>
<td>HXR6104-EVB</td>
<td>Room Temp</td>
<td>Evaluation Board</td>
</tr>
</tbody>
</table>

For price, delivery schedules, and to place orders, please contact IDT: www.IDT.com/go/sales

Device Diagram

![Device Diagram](image)

Figure 1: Device diagram