Layout notes:
1. Separate CLKINB/X2 and CLKIN/X1 trace by 3 x the trace width.
2. Do not share crystal load capacitor ground via with other components.
3. Route power from bead through bulk capacitor pad then through 0.1uF capacitor pad then to clock chip Vdd pad.
4. Do not share ground vias. One ground pin one ground via.

NOTE: FERRITE BEAD FB1 =
Manufacture Part Number 20100MHz Pkg Sz DC res. Current (mA)
Fair-Rite 2504021217Y0 120 0402 0.5 200
muRata BLM15AG221SN1 220 0402 0.35 300
muRata BLM15BB121SN1 120 0402 0.35 300
TDK MM2005S241A 240 0402 0.18 200
TECSTAR TB4532153121 120 0402 0.3 300

Output Driver VDDo
Serial Resistor
VDDo = 1.8V 22 ohms
VDDo = 1.2, 1.1 and, 1 V 20 ohms

Integrated Device Technology

Title
5X1503 / SL1503 Reference Schematic

Size
A

Document Number
<Doc>

Rev
.1

Date: Monday, May 13, 2019
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