Maximum voltage on pin 1 = 1.8V
Place close to ClockMatrix
Notes:
1. By connecting I2C to the header's pin 1 and 3, this is to give I2C access to external I2C master.
2. By installing R926 and R927, FTDI can also access Aux I2C port (also removing R87 and R88). This feature is not expected to be used.
3. Normal use: FTDI controls I2C port; Header is connected to Aux port in SPI configuration.
Note for Silkscreen:
Place GPIO[x] labels close to each corresponding LED and Test point.
PLL Input Clocks

- 3.3V clock source
- 12.8MHz clock source
- Various components like capacitors, inductors, and diodes
- Diagram includes pin references and component placements

Diagram details:
- VCC, GND, VDD, etc.
- Component values and specifications
- Connection points and labels
- Layout for PCB design

Notes:
- Indira Systems
- 375 Terry Fox Drive, Ottawa, ON K2K 0J8
- 27 October 2016
- Design specification and notes